

# JUWELS & JURECA Tuning for the platform

#### **Usage of ParaStation MPI**

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Outline



# **JUWELS & JURECA**

Tuning for the platform

- 1. ParaStation MPI
- 2. Compiling your program
- 3. Running your program
- 4. Tuning parameters
- 5. Resources





# **History of ParaStation**

- 1995: University project ( $\rightarrow$  University of Karlsruhe)
- 2005: Open source ( $\rightarrow$  ParaStation Consortium)
- Since 2004: Cooperation with JSC
  - various precursor clusters
  - DEEP-System (MSA prototype)
  - JuRoPA3 (J3)
  - JUAMS
  - JURECA (Cluster/Booster)
  - JUWELS (Cluster/Booster)
  - JURECA DC
  - JUPITER









- based on MPICH (4.1.1)
  - supports all MPICH tools (tracing, debugging, ...)
- proven to scale up to 3,300 nodes and 136.800 procs per job running ParaStation MPI
  - JURECA DC: No. 102 (Top500 Jun 2024), No. 45 (Green500 Jun 2024)
  - JUWELS Booster: No. 21 (Top500 Jun 2024), No. 44 (Green500 Jun 2024)
  - JEDI: No. 189 (Top500 Jun 2024), No. 1 (Green500 Jun 2024)
- supports a wide range of interconnects, even in parallel
  - InfiniBand on JURECA DC and JUWELS
  - Omni-Path on JURECA Booster (deprecated)
  - Extoll on DEEP projects research systems (deprecated)
- tight integration with Cluster Management (e.g. healthcheck)
- MPI libraries for several compilers
  - especially for GCC and Intel



- 2 or more different modules with different hardware
- a job can execute dynamically on all modules

• you can pick the best out of all the worlds in a single job

• e.g. JURECA:

- DC: AMD EPYC + Nvidia A100 + Infiniband
- Booster: Intel KNL + Omni-Path

• how do these modules communicate with each other?





- Iow-level communication layer supporting various transports and protocols
- applications may use multiple transports at the same time





- If or the JURECA DC-Booster System, the ParaStation MPI Gateway Protocol bridges between Mellanox IB and Intel Omni-Path
- In general, the ParaStation MPI Gateway Protocol can connect any two low-level networks supported by pscom
- implemented using the *psgw* plugin to pscom, working together with instances of the *psgwd*



- two processes communicate through a gateway, if they are not directly connected by a high-speed network (e.g. IB or OPA)
- static routing to choose a common gateway
- high-speed connections between processes and gateway daemons
- virtual connection between both processes through the gateway, transparent for application
- virtual connections are multiplexed through gateway connections
- further information: <u>apps.fz-juelich.de/jsc/hps/jureca/modular-jobs.html</u>



- CUDA awareness supported by the following MPI APIs
  - Point-to-point (e.g. MPI\_SEND, MPI\_RECV, ...)
  - Collectives (e.g. MPI\_Allgather, MPI\_Reduce, ...)
  - One-sided (e.g. MPI\_Put, MPI\_Get, ...)
  - Atomics (e.g. MPI\_Fetch\_and\_op, MPI\_Accumulate, ...)
- CUDA awareness for all transports via staging
- CUDA optimization: UCX
- ability to query CUDA awareness at compile- and runtime



- activate CUDA awareness by meta modules
  - default configurations
- query CUDA awareness:

```
#if defined(MPIX_CUDA_AWARE_SUPPORT) && MPIX_CUDA_AWARE_SUPPORT
printf("The MPI library is CUDA-aware\n");
#endif

if (MPIX_Query_cuda_support())
    printf("The MPI library is CUDA-aware\n");
```



- currently MPI-4 version (5.10.0-1) available
- single thread tasks
  - module load Intel ParaStationMPI
  - module load GCC ParaStationMPI
- multi-thread tasks (mt)
  - module load Intel ParaStationMPI/5.10.0-1-mt
  - no multi-thread GCC version available
- ChangeLog available with
  - less \$(dirname \$(which mpicc))/../ChangeLog
- Gnu and Intel compilers available
- module spider for getting current versions
- see also the previous talk JUWELS Introduction

#### Wrapper



#### • Wrappers

- mpicc(C)
- mpicxx (C++)
- mpif90 (Fortran 90)
- mpif77 (Fortran 77)
- when using OpenMP and the need to use the "mt" version, add
  - -fopenmp (GNU)
  - -qopenmp (Intel)

#### Did I use the wrapper correctly?



- libaries are linked at runtime according to LD LIBRARY PATH
- 1dd shows the libraries attached to your binary
- Iook for ParaStation libraries

```
ldd hello_mpi:
...
libmpi.so.12 => /p/software/juwels/stages/2020/
software/psmpi/5.10.0-1-iccifort-2020.2.254-GCC-9.3.0/
lib/libmpi.so.12 (0x000015471ea43000)
...
vs.
...
libmpi.so.12 => /p/software/juwels/stages/2020/
software/psmpi/
5.10.0-1-iccifort-2020.2.254-GCC-9.3.0-mt/lib/
libmpi.so.12 (0x000014f110e58000)
...
```

#### JUWELS: start via srun



- use **srun** to start MPI processes
- srun -N <nodes> -n <tasks> spawns task
  - directly (-A <account>)
  - via salloc
  - from batch script via **sbatch**
- exports full environment
- stop interactive run with (consecutive) ^C
  - passed to all tasks
- no manual clean-up needed
- you can log into nodes which have an allocation/running job step
  - squeue -u <user>
  - sgoto <jobid> <nodenumber>
    - e.g. sgoto 2691804 0



```
/* C Example */
#include <stdio.h>
#include <mpi.h>
int main (int argc, char **argv) {
  int numprocs, rank, namelen;
  char processor name [MPI MAX PROCESSOR NAME];
 MPI Init (&argc, &argv);
 MPI Comm rank (MPI COMM WORLD, &rank);
 MPI Comm size (MPI COMM WORLD, &numprocs);
 MPI Get processor name (processor name, &namelen);
 printf ("Hello world from process %d of %d on %s\n",
         rank, numprocs, processor name);
 MPI Finalize ();
  return 0;
```



- module load Intel
- module load ParaStationMPI
- mpicc -03 -o hello\_mpi hello\_mpi.c

#### • Interactive:

- salloc -N 2 -A partec # get an allocation
- srun -n 2 ./hello\_mpi

# Hello world from process 0 of 2 on jwc08n188.juwels Hello world from process 1 of 2 on jwc08n194.juwels

#### • Batch:

• sbatch ./hello\_mpi.sh

#### • Increase verbosity:

PSP\_DEBUG=[1,2,3,...] srun -n 2 ./hello\_mpi



- ParaStation process pinning:
  - avoid task switching
  - make better use of CPU cache and memory bandwidth
- JUWELS is pinning by default:
  - **so** --cpu-bind=threads **may be omitted**
- manipulate pinning:
  - e.g. for "large memory / few task" applications
- manipulate via
  - --cpu-bind=threads|sockets|cores|mask\_cpu:<mask1>,<mask2>,...
    - CPU masks are always interpreted as hexadecimal values
  - --distribution=\*|block|cyclic|arbitrary|plane=<options> [:\*|block| cyclic|fcyclic[:\*|block|cyclic|fcyclic]][,Pack|NoPack]
- Inter information: <u>https://apps.fz-juelich.de/jsc/hps/juwels/affinity.html</u>

#### **Process Placement**



- Example:
  - --ntasks-per-node=4
  - --cpus-per-task=3
- --cpu-bind=threads

0	0	2	2	1	1	3	3		
0		2		1		3			

● --cpu-bind=mask\_cpu:0x7,0x700,0xE0,0xE000

0	0	0		2	2	2	1	1	1		3	3	3



- best practice depends not only on topology, but also on characteristics of application:
- putting threads far apart is
  - improving the aggregated memory bandwidth available to your application
  - improving the combined cache size available to your application
  - decreasing the performance of synchronization constructs
- putting threads close together is
  - improving the performance of synchronization constructs
  - decreasing the available memory bandwidth and cache size

## Hybrid MPI/OpenMP



Node y

P2

**P**3

```
#include <stdio.h>
#include <mpi.h>
                                                              Example:
#include <omp.h>
                                                              2 Nodes, 2x2 Procs,
                                                              2x2x24 Threads
int main(int argc, char *argv[]) {
 int numprocs, rank, namelen;
 char processor name [MPI MAX PROCESSOR NAME];
                                                                Node x
 int iam = 0, np = 1;
 MPI Init(&argc, &argv);
                                                                     P1
                                                               P0
 MPI Comm size (MPI COMM WORLD, &numprocs);
 MPI Comm rank (MPI COMM WORLD, &rank);
 MPI Get processor name (processor name, &namelen);
#pragma omp parallel default(shared) private(iam, np)
   np = omp get num threads();
   iam = omp get thread num();
   printf("Hello from thread %02d out of %d from process %d out of %d on %s\n",
          iam, np, rank, numprocs, processor name);
  }
 MPI Finalize();
```

## **On JUWELS**



• module load Intel ParaStationMPI/5.10.0-1-mt

• mpicc -03 -qopenmp -o hello\_hybrid hello\_hybrid.c

```
• salloc -N 2 -A partec -cpus-per-task=24
```

```
• export OMP NUM THREADS=${SLURM CPUS PER TASK}
```

• srun -n 4 ./hello hybrid | sort

Hello from thread 00 out of 24 from process 0 out of 4 on jwc01n238.juwels Hello from thread 00 out of 24 from process 1 out of 4 on jwc01n238.juwels Hello from thread 00 out of 24 from process 2 out of 4 on jwc01n247.juwels Hello from thread 00 out of 24 from process 3 out of 4 on jwc01n247.juwels Hello from thread 01 out of 24 from process 0 out of 4 on jwc01n238.juwels Hello from thread 01 out of 24 from process 1 out of 4 on jwc01n238.juwels Hello from thread 01 out of 24 from process 2 out of 4 on jwc01n238.juwels Hello from thread 01 out of 24 from process 2 out of 4 on jwc01n238.juwels Hello from thread 01 out of 24 from process 3 out of 4 on jwc01n238.juwels Hello from thread 01 out of 24 from process 3 out of 4 on jwc01n247.juwels

Hello from thread 23 out of 24 from process 0 out of 4 on jwc01n238.juwels Hello from thread 23 out of 24 from process 1 out of 4 on jwc01n238.juwels Hello from thread 23 out of 24 from process 2 out of 4 on jwc01n247.juwels Hello from thread 23 out of 24 from process 3 out of 4 on jwc01n247.juwels

## Pinning



#### • JUWELS:

- 2 Sockets, 24 Cores per Socket
- 2 HW-Threads per Core
- \*  $\rightarrow$  96 HW-Threads possible
- normally (SMT):
  - HW-Threads 0-23, 48-71  $\rightarrow$  CPU0
  - HW-Threads 24-47, 72-95  $\rightarrow$  CPU1

	"Packac	le"
_	i donag	,

Node												
	S	ocket	0 🔨		Socket 1							
Core 0	Core 1		Core 22	Core 23	Core 24	Core 25		Core 46	Core 47			
HWT 0	HWT 1		HWT 22	HWT 23	HWT 24	HWT 25		HWT 46	HWT 47			
HWT 48	HWT 49		HWT 70	HWT 71	HWT 72	HWT 73		HWT 94	HWT 95			

## Pinning



#### • JURECA DC:

- 2 Sockets, 64 Cores per Socket
- 2 HW-Threads per Core
- \*  $\rightarrow$  256 HW-Threads possible
- normally (SMT):
  - HW-Threads 0-63, 128-191  $\rightarrow$  CPU0
  - HW-Threads 64-127, 192-255 → CPU1

"Package"

Node	Vode												
	S	ocket	0 🔨		Socket 1 🔺								
Core 0	Core 1		Core 62	Core 63	Core 64	Core 65		Core 126	Core 127				
HWT 0	HWT 1		HWT 62	HWT 63	HWT 64	HWT 65		HWT 126	HWT 127				
HWT 128	HWT 129		HWT 190	HWT 191	HWT 192	HWT 193		HWT 254	HWT 255				



- no thread pinning by default on JURECA and JUWELS
- Illow the Intel OpenMP library thread placing
  - export KMP\_AFFINITY=[verbose, modifier, ...]
    - compact: place threads as close as possible
    - scatter: as evenly as possible
- I full environment is exported via srun on JURECA and JUWELS
- for GCC: set GOMP\_CPU\_AFFINITY (see manual)



- every MPI process talks to all others:
  - (N-1) x 0.55 MB communication buffer space per process!
- example 1 on JUWELS:
  - job size 256 x 96 = 24,576 processes
  - \* 24,575 x 0.55 MB  $\rightarrow$  ~ 13,516 MB / process
  - x 96 processes / node  $\rightarrow$  ~ 1,267 GB communication buffer space
  - but there is only 96 GB of main memory per node
- example 2 on JURECA DC:
  - job size 256 x 256 = 65,536 processes
  - \* 65,535 x 0,55 MB  $\rightarrow$  ~ 36,044 MB / process
  - x 256 processes / node  $\rightarrow \sim 9,011$  GB communication buffer space
  - but there is only 512 GB of main memory per node

# **On Demand / Buffer Size**

- Three possible solutions:
- I. Try using alternative meta modules
- 2. Create buffers on demand only:
  - export PSP\_ONDEMAND=1
  - activated by default!
- 3. Reduce the buffer queue length:
  - (default queue length is 16)
  - export PSP\_OPENIB\_SENDQ\_SIZE=3
  - export PSP\_OPENIB\_RECVQ\_SIZE=3
  - do not go below 3, deadlocks might occur!
  - trade-off: performance penalty
    - (sending many small messages)







- (Halo) Exchange
- Scatter/Gather
- All-reduce
- ...
- but for All-to-All communication:
  - queue size modification only viable option...
- example





#### Resources



- <u>www.par-tec.com</u>
- www.fz-juelich.de/en/ias/jsc/systems/supercomputers
- Intersection (doc/pdf)
- by mail: <u>sc@fz-juelich.de</u>
- by mail: support@par-tec.com
- download ParaStation MPI at github:
  - <u>https://github.com/ParaStation/psmgmt</u>
  - <u>https://github.com/ParaStation/pscom</u>
  - <u>https://github.com/ParaStation/psmpi</u>

# Summary



- you now should be able to
  - compile
  - run your application
  - tune some runtime parameters
  - diagnose and fix specific errors
  - know where to turn to in case of problems



# Questions?

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